

<b>Notice of References Cited</b>	Application/Control No. 10/812,150	Applicant(s)/Patent Under Reexamination SMITH ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0103945	08-2002	Owen et al.	710/10
*	B	US-2003/0110338	06-2003	Wang et al.	710/305
*	C	US-2002/0172195	11-2002	Pekkala et al.	370/360
*	D	US-2002/0071450	06-2002	Gasbarro et al.	370/463
*	E	US-6,665,742	12-2003	Owen et al.	710/10
*	F	US-6,697,352	02-2004	Ludwig et al.	370/349
*	G	US-6,516,442	02-2003	Wang et al.	714/776
*	H	US-6,857,033	02-2005	Gulick et al.	710/62
*	I	US-6,591,310	07-2003	Johnson, Stephen B.	710/3
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"a supercomputer system interconnect and scalable IOS" by Johnson, B.; Scott, S. (abstract only) Publication Date: 11-14 Sept. 1995
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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